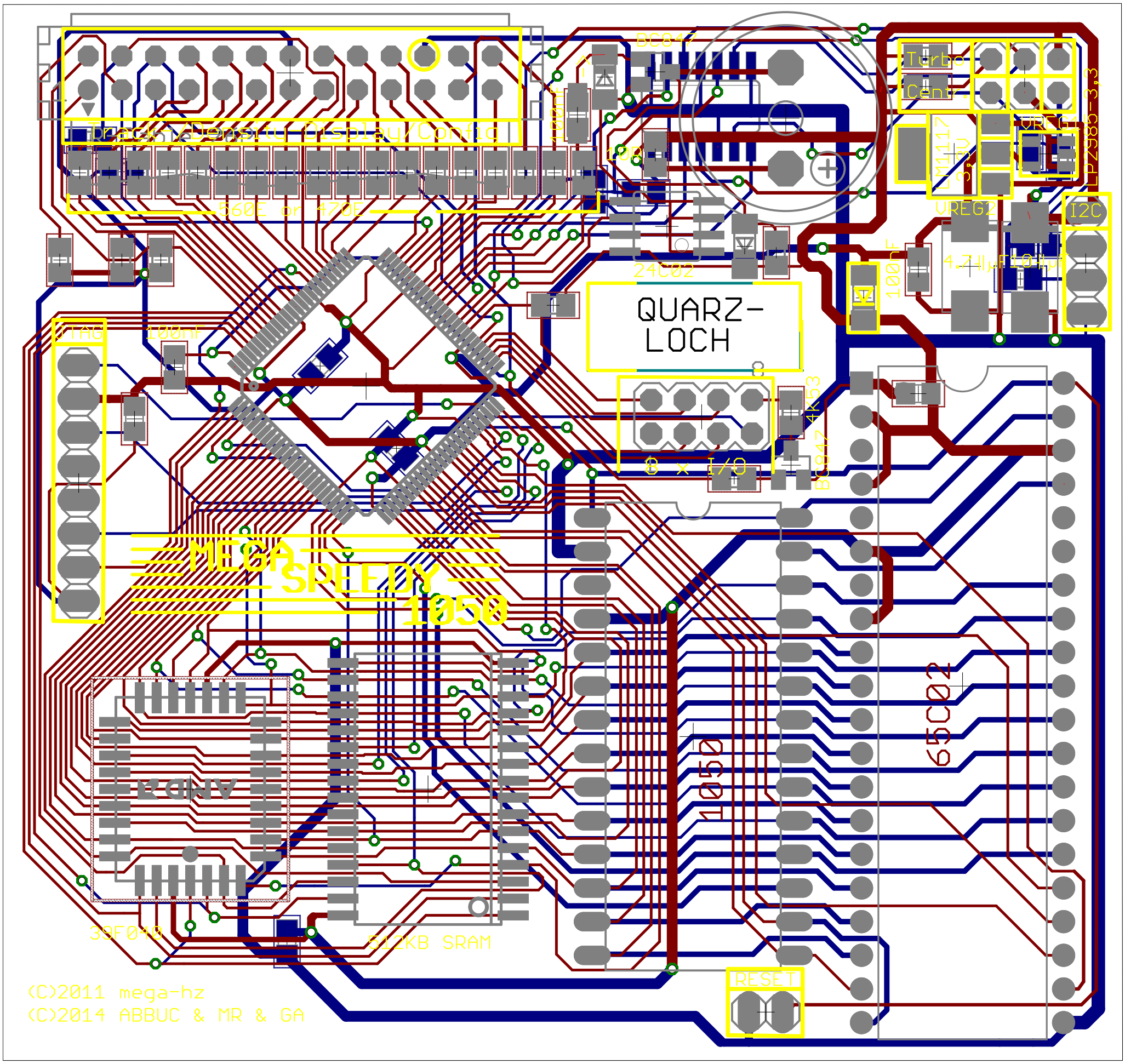
Header Dessite Display Config
560E or 470E
BC847
100nF
10uH
120R
24C02
QUARZ-
LOCH
8 x I/O
4K53
BC847
Turbo
Cent.
VREG1
LM1117
3,3V
VREG2
LP2985-3,3
I2C
4,7µF
470µF
100nF
JTAG
100nF
MEGA
SPEEDY
1050
65C02
1050
39F040
512KB SRAM
RESET
(C)2011 mega-hz
(C)2014 ABBUC & MR & GA